



Call for Papers DCPerf '11

June 24, 2011
Minneapolis, MN, USA
<http://www.zurich.ibm.com/dcperf11>

The First International Workshop on Data Center Performance

Data centers are the backbone infrastructure for tomorrow's information technology. Their advantages are efficient resource provisioning and low operational costs for supporting a wide range of computing needs, be it in business, scientific or mobile/pervasive environments. The high volume of mixed workloads and the diversity of services offered render the performance optimization of data centers ever more challenging. Moreover, important optimization criteria, such as scalability, reliability, manageability, power efficiency, area density, operating cost and many more, often are mutually exclusive to some extent.

We seek submissions of papers that present new techniques, introduce new methodologies, propose new research directions or discuss strategies for resolving open performance problems at all layers of a data center. Focus will be on the data center system level aspects, data center communication, and virtualization & performance optimization.

Topics of interest include (but are not limited to)

- Data Center Communication
 - Intra/Inter DC communication
 - Network Protocols
 - Security
- Data Center Performance
 - Power
 - Reliability
- Cloud Computing on Data Center
- Data Center Performance Evaluation
 - Data mining
- Simulation
- Modeling
- Data Center Systems
 - DC architectures
 - DC software/applications
 - Empirical studies
- Data Center Virtualization
 - Hardware support
 - Hypervisors
 - Virtualized storage

Important date

Paper Registration and Submission: ~~Jan 24th 2011~~, **Feb 10th 2011**

Notification of Acceptance: Mar 7th 2011

Final Manuscript Due: Mar 30th 2011

Submission Guideline

Manuscripts must be limited to 6 pages plus up to 2 over-length pages in IEEE 8.5x11 format. Accepted papers will be published in the combined ICDCS 2011 workshop proceedings and will be available through IEEE Xplore. Best papers will be solicited for recommended submissions to IEEE Computer Architecture Letters (CAL).

Organizing Committee

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Peter Muller, IBM Zurich Research Lab, Switzerland

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